

Fig. 1  
Prior Art

FIG. 2A is a block diagram of a digital image processing system 200. The system 200 includes a digital image source 204, an analog image source 206, an A/D converter 208, a digital image signal 209, a digital image processing engine 212, a resultant proxy image input signal, a resultant image, a digital negative, an edit-list source 213, an edit-list stream 210, an output unit 216, an output port 220, and a request more data from system, per edit list.

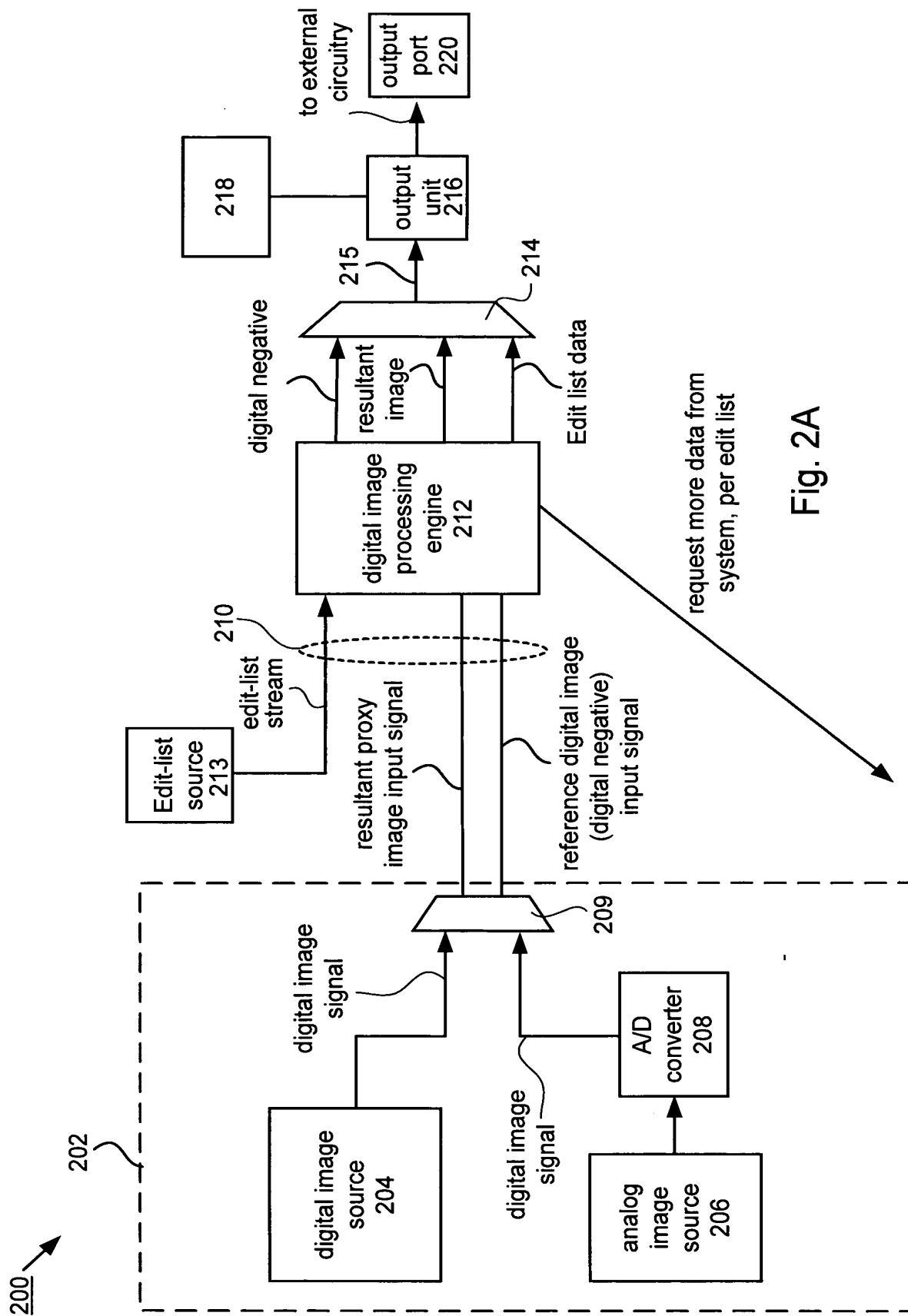


Fig. 2A

FIG. 2B is a block diagram of a system 212 for processing digital negative images. The system 212 includes an input controller 250, an image processor 252, and an edit list processor 254. The input controller 250 receives a digital negative (reference image) and a proxy/resulting image. It sends data to the image processor 252 and the edit list processor 254. The image processor 252 outputs a modified proxy/resulting image. The edit list processor 254 outputs a modified proxy/resulting image + edit list data and a pointer to the modified proxy/resulting image. The edit list processor 254 also outputs edit list data. The input controller 250 can request more data per edit list.

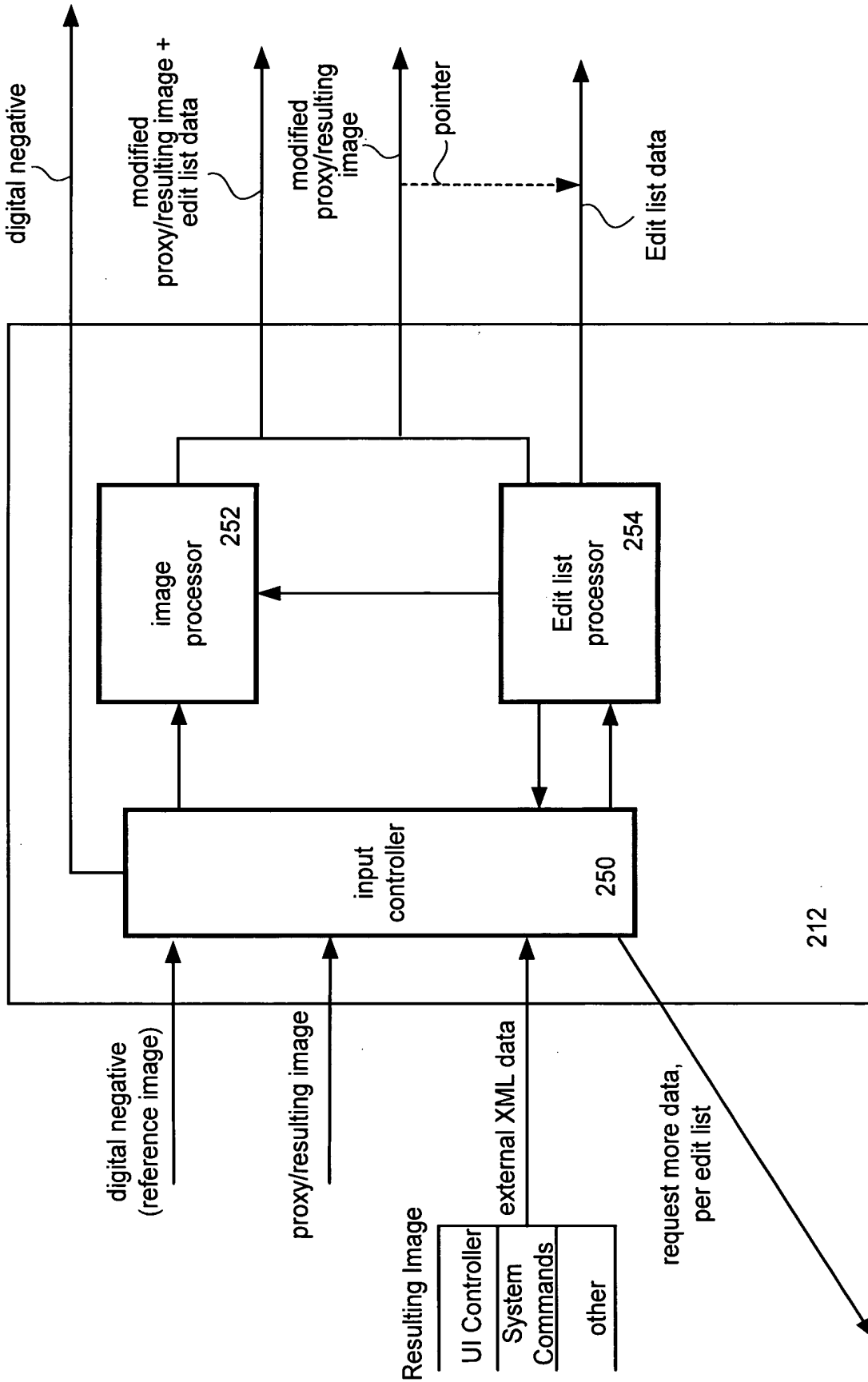


Fig. 2B

FIG. 2c is a block diagram of a digital image editing system 300. The system 300 includes a digital image 302 and an edit list 304. The digital image 302 is a large rectangular area. The edit list 304 is a smaller rectangular area located to the right of the digital image 302. An arrow points from the edit list 304 to the digital image 302, indicating that the edit list 304 is used to edit the digital image 302.

300

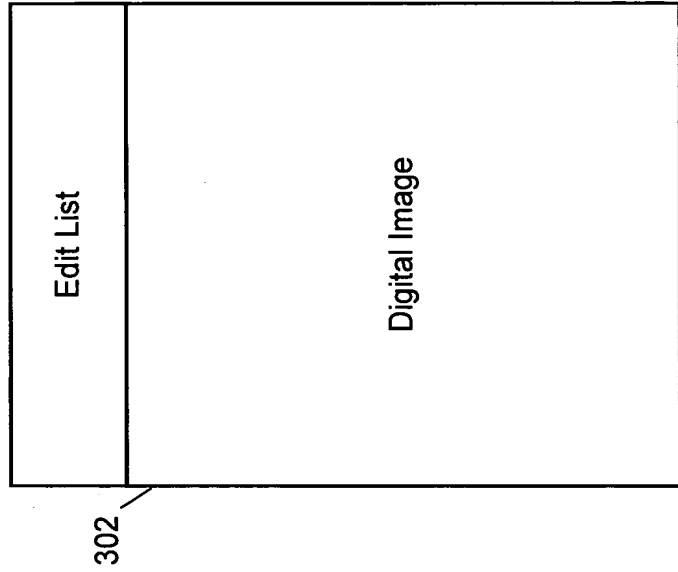


Fig. 2c

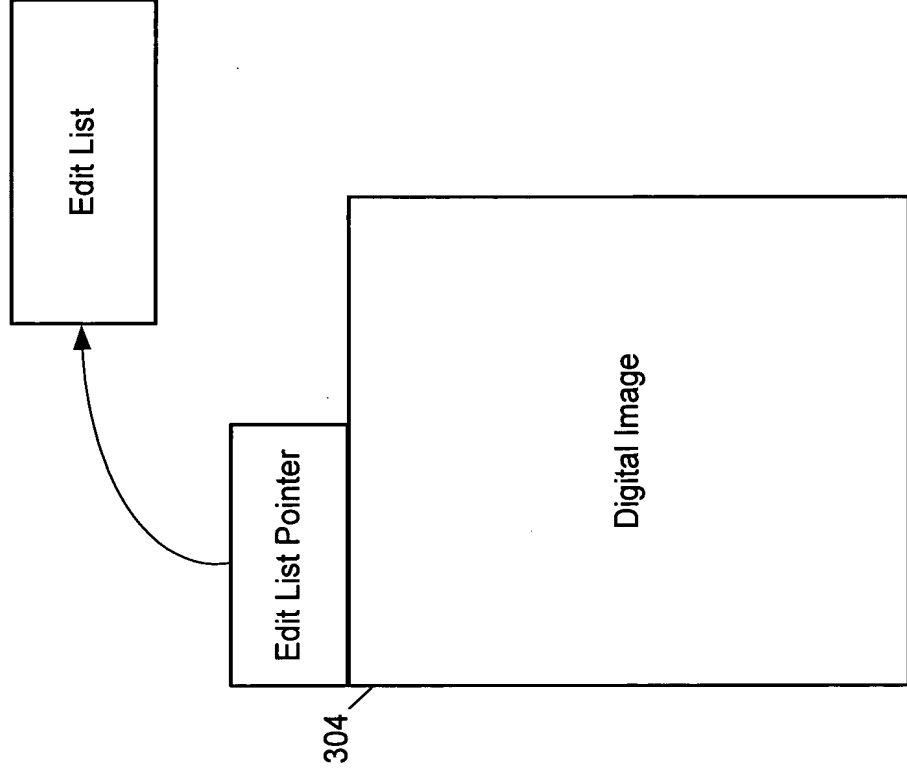
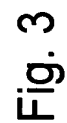


Fig. 2d



**Fig. 3**

950

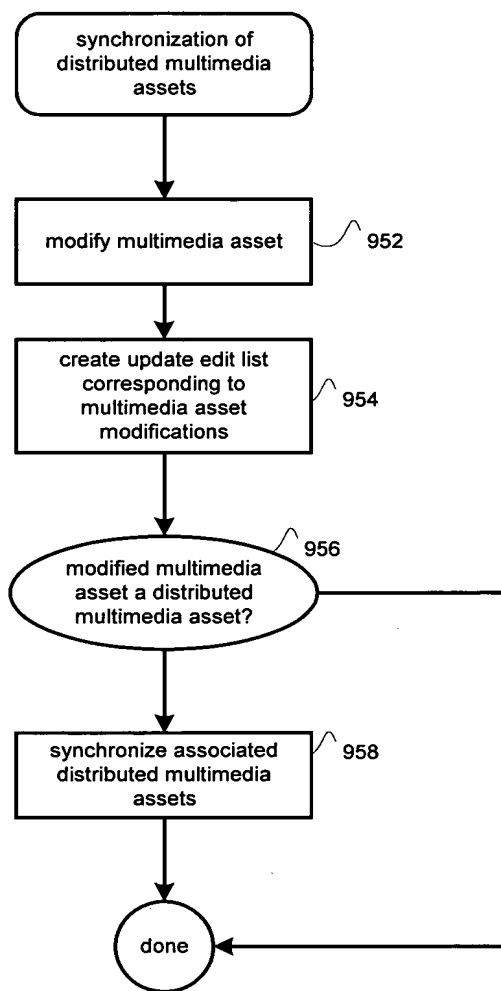
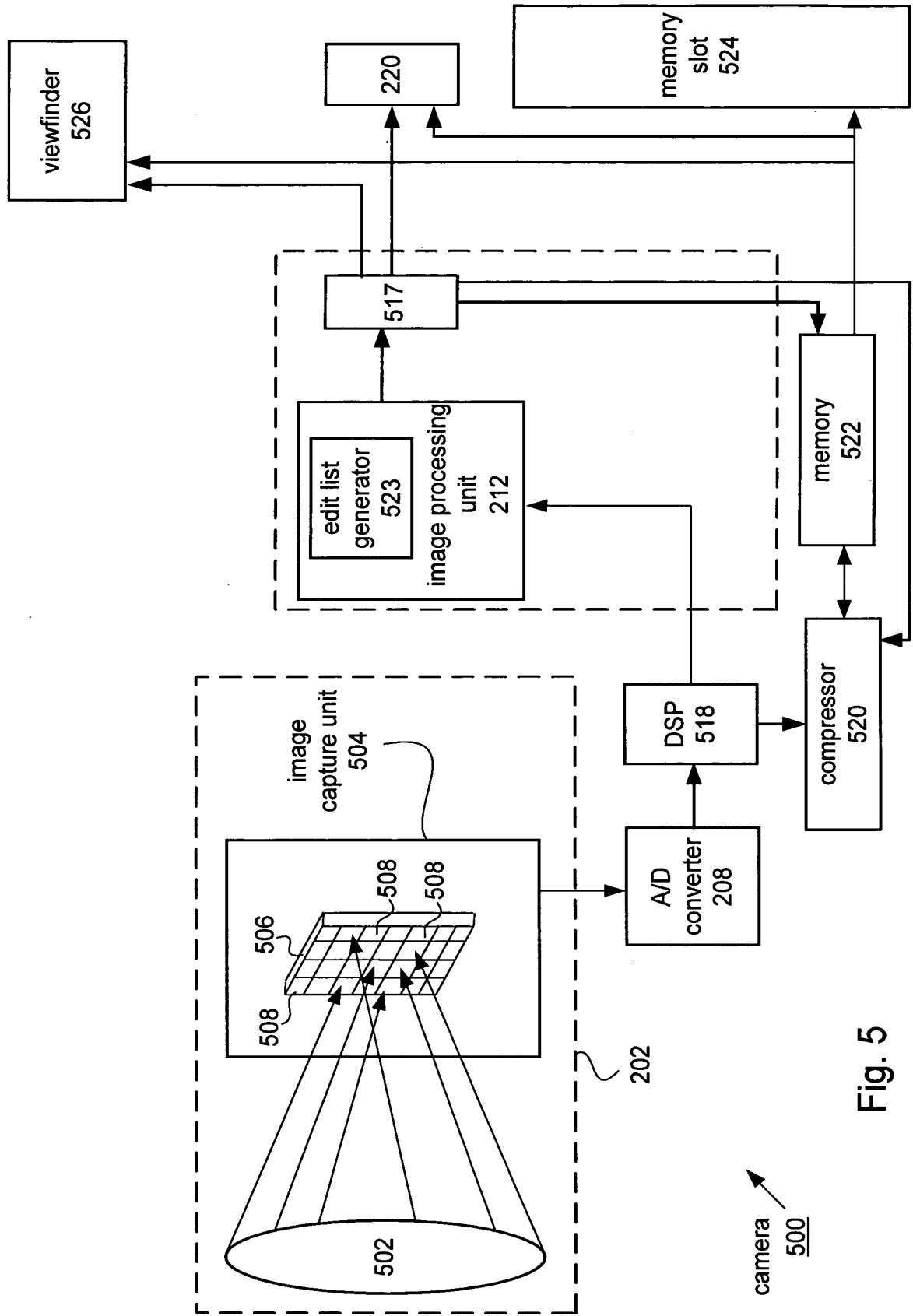


Fig. 4



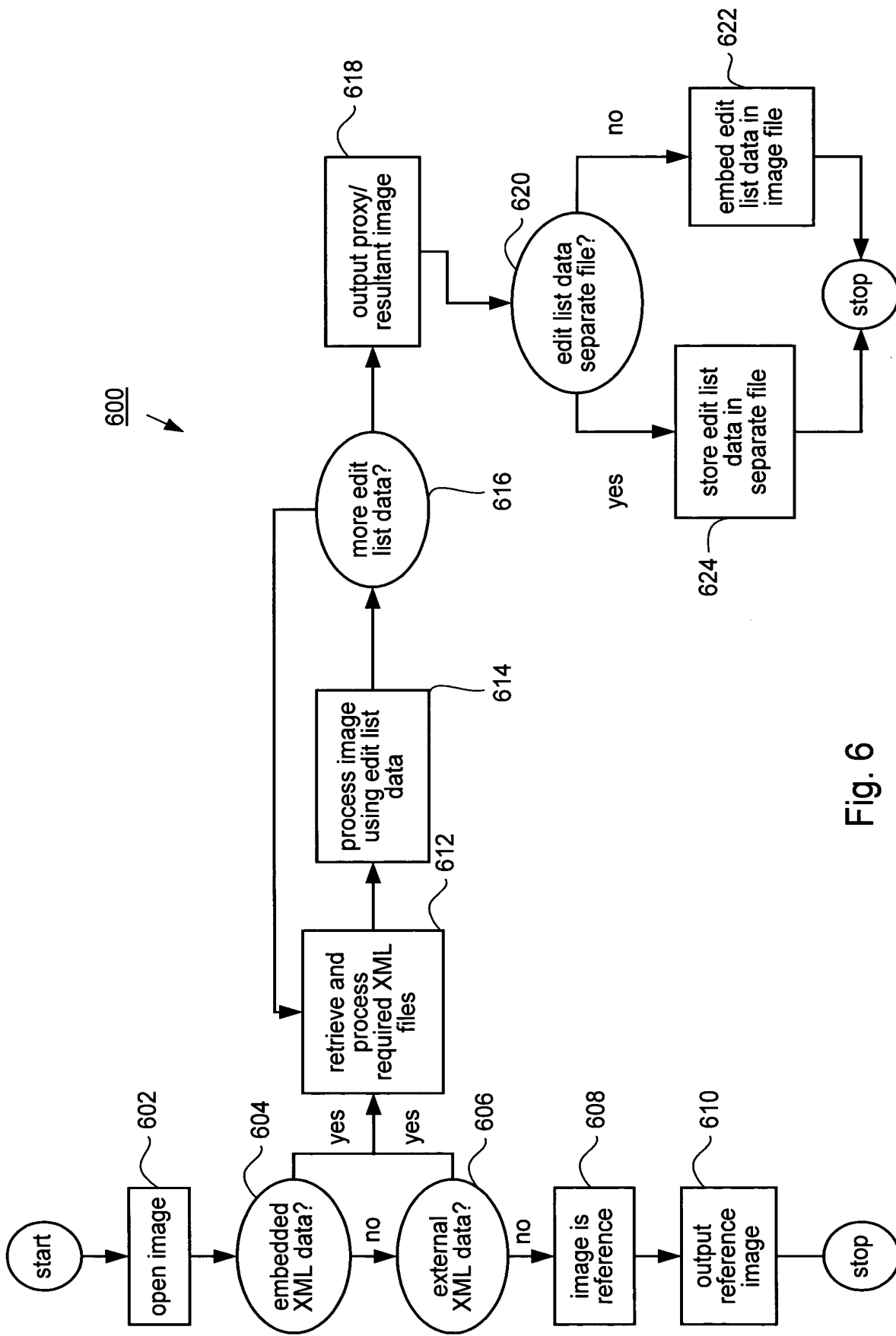


Fig. 6



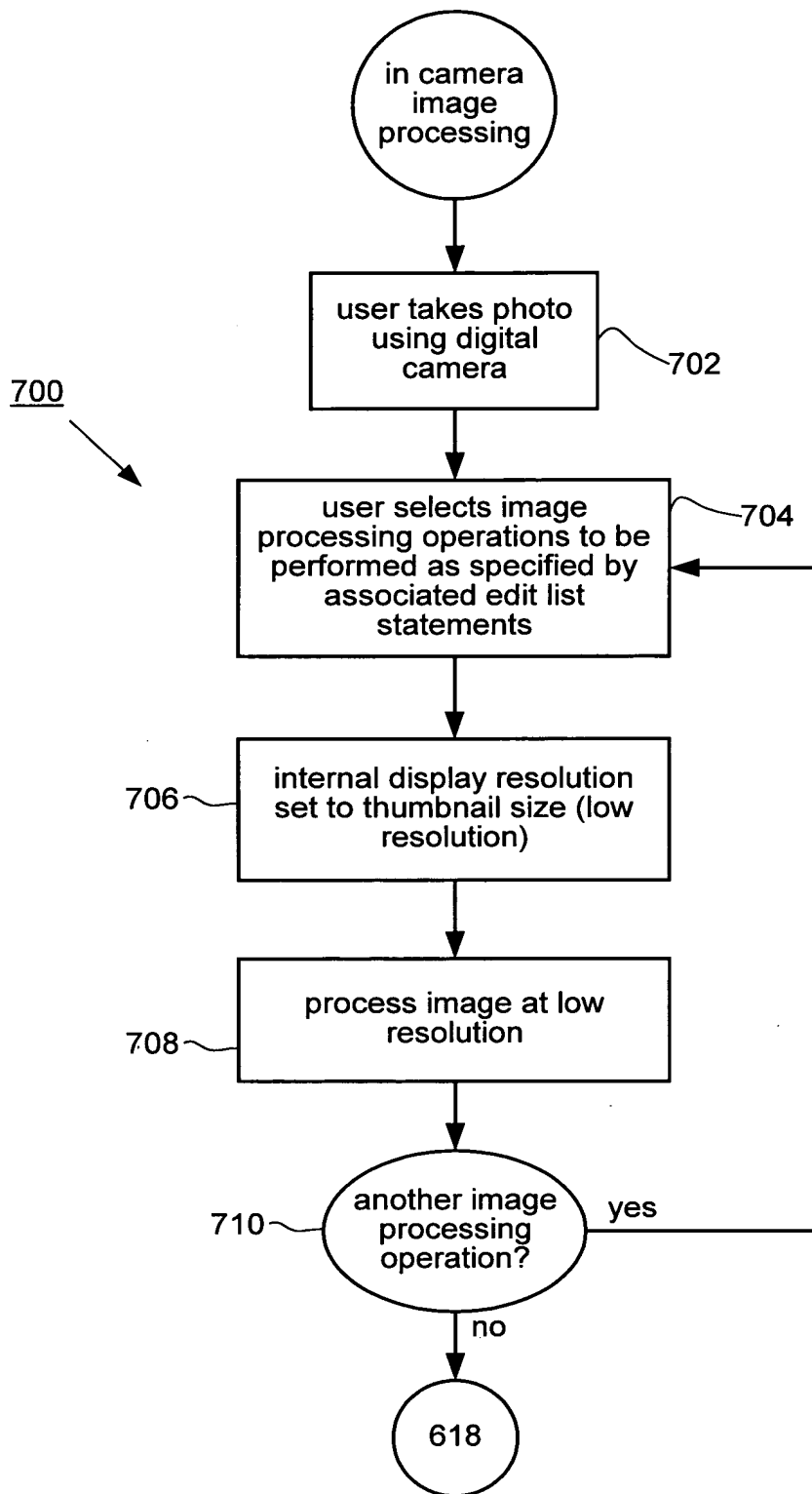


Fig. 7

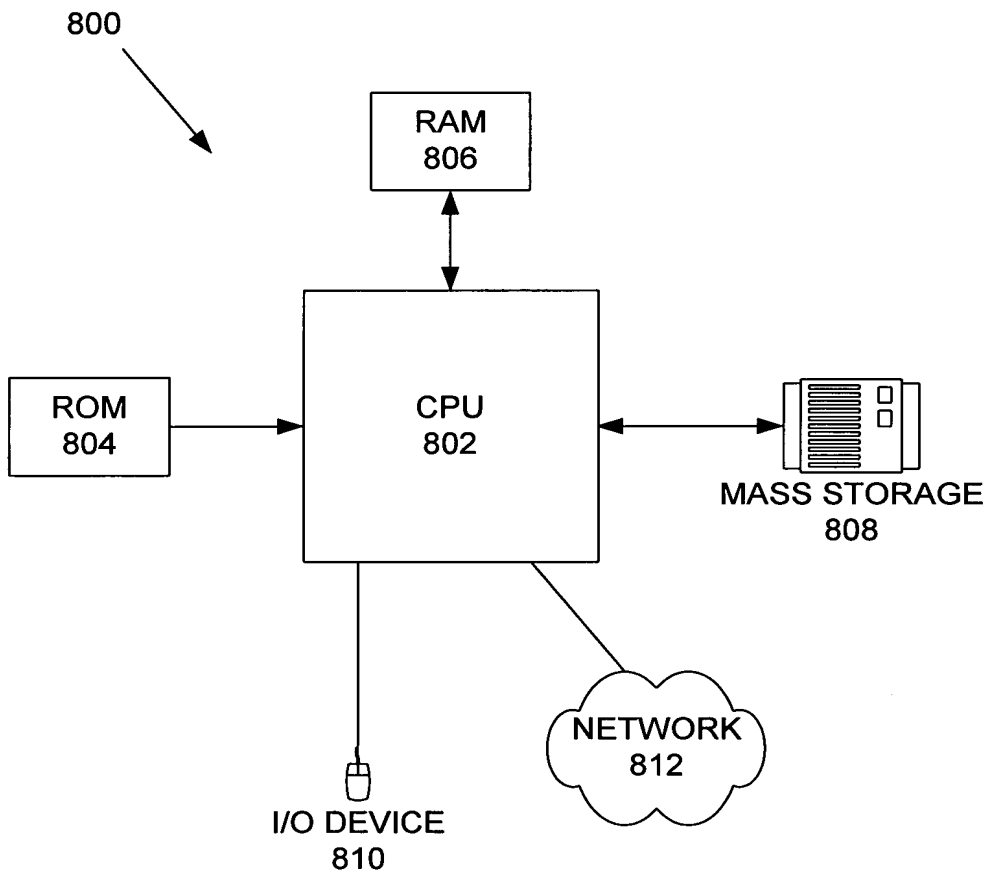


Fig. 8